

Amendments to the Claims

This listing of claims replaces all prior versions and listings of claims in the application.

Listing of Claims

1. (Currently amended) A current source circuit ~~characterized in by~~ comprising:
a plurality of current sources, each of which is configured to receive an external set signal
and to can control an output current value in response to the external by a set signal; and
~~inputted from outside;~~

~~wherein a changing over means which can change over an electrical connection between~~
~~an output line and the plurality of current sources is provided between the output line and the~~
~~plurality of current sources~~

a changing over means electrically connected to the plurality of current sources and a
plurality of output lines,

wherein the changing over means selects one current source from the plurality of current
sources for electrically connecting to each of the plurality of output lines.

2. (Currently amended) A current source circuit comprising: ~~characterized in by~~
~~comprising a plurality of pairs of current sources which can control an output current value by a~~
~~set signal inputted from outside;~~

~~wherein a changing over means which can change over an electrical connection between~~
~~an output line and a plurality of current sources is provided between the output line and the~~
~~plurality of pairs of current sources~~

a plurality of pairs of current sources, each of which is configured to receive an external
set signal and to control an output current value in response to the external set signal; and

a changing over means electrically connected to the plurality of pairs of current sources
and a plurality of output lines,

wherein the changing over means selects one pair of current sources from the plurality of pairs of current sources for electrically connecting to each of the plurality of output lines.

3. (Currently amended) A signal line driver circuit comprising: ~~characterized in by comprising a plurality of current sources which can control an output current value by a set signal inputted from outside, a shift resistor, a first latch circuit, and a second latch circuit,~~

~~wherein a changing over means which can change over an electrical connection between a signal line and the plurality of current sources is provided between the signal line and the plurality of current sources, and~~

~~wherein the set signal is set according to the shift register~~

a shift register;

first and second latch circuits electrically connected to the shift register;

a plurality of current sources, each of which is configured to receive a set signal and to control an output current value in response to the set signal; and

a changing over means electrically connected to the plurality of current sources and a plurality of signal lines,

wherein the changing over means selects one current source from the plurality of current sources for electrically connecting to each of the plurality of signal lines, and

wherein the set signal is an output of the shift register.

4. (Currently amended) A signal line driver circuit comprising: ~~characterized in by comprising a plurality of current sources which can control an output current value by a set signal inputted from outside, a shift resistor, a first latch circuit, a second latch circuit, and switches provided in the plurality of current sources,~~

~~wherein a changing over means which can change over an electrical connection between a signal line and the plurality of current sources is provided between the signal line and the plurality of current sources,~~

~~wherein the set signal is set according to the shift register, and~~

~~the switches are controlled by a signal from the second latch circuit;~~
a shift register;
first and second latch circuits electrically connected to the shift register;
a plurality of current sources, each of which is configured to receive a set signal and to
control an output current value in response to the set signal;
a changing over means electrically connected to the plurality of current sources and a
plurality of signal lines; and
a switch provided between the changing over means and each of the plurality of current
sources,
wherein the changing over means selects one current source from the plurality of current
sources for electrically connecting to each of the plurality of signal lines,
wherein the set signal is an output of the shift register, and
wherein the switch is controlled by a signal from the second latch circuit.

5. (Currently amended) A signal line driver circuit comprising: ~~characterized in by~~
~~comprising a plurality of current sources which can control an output current value by a set~~
~~signal inputted from outside, a shift resistor, a first latch circuit, a second latch circuit, and~~
~~switches provided in the plurality of current sources;~~

~~wherein a changing over means which can change over an electrical connection between~~
~~a signal line and the plurality of current sources is provided between the signal line and the~~
~~plurality of current sources, and~~

~~wherein the set signal is set according to the second latch circuit~~
a shift register;
first and second latch circuits electrically connected to the shift register;
a plurality of current sources, each of which is configured to receive a set signal and to
control an output current value in response to the set signal;
a changing over means electrically connected to the plurality of current sources and a
plurality of signal lines; and

a switch provided between the changing over means and each of the plurality of current sources,

wherein the changing over means selects one current source from the plurality of current sources for electrically connecting to each of the plurality of signal lines, and
wherein the set signal is set according to the second latch circuit.

6. (Currently amended) A signal line driver circuit comprising: ~~characterized in by~~
~~comprising a plurality of current sources which can control an output current value by a set~~
~~signal inputted from outside, a shift resistor, a first latch circuit, a second latch circuit, and~~
~~switches provided in the plurality of current sources,~~

~~wherein a changing over means which can change over an electrical connection between~~
~~a signal line and the plurality of current sources is provided between the signal line and the~~
~~plurality of current sources;~~

~~wherein the set signal is set according to the second latch circuit, and~~

~~wherein the switches are controlled by a signal from the second latch circuit~~

~~a shift register;~~

~~first and second latch circuits electrically connected to the shift register;~~

~~a plurality of current sources, each of which is configured to receive a set signal and to~~
~~control an output current value in response to the set signal;~~

~~a changing over means electrically connected to the plurality of current sources and a~~
~~plurality of signal lines; and~~

~~a switch provided between the changing over means and each of the plurality of current~~
~~sources,~~

~~wherein the changing over means selects one current source from the plurality of current~~
~~sources for electrically connecting to each of the plurality of signal lines,~~

~~wherein the set signal is set according to the second latch circuit, and~~

~~wherein the switch is controlled by a signal from the second latch circuit.~~

7. (Currently amended) A signal line driver circuit comprising: ~~characterized in by~~
~~comprising a plurality of current sources which can control an output current value by a set~~
~~signal inputted from outside, a first shift register, a second shift register, a first latch circuit, and~~
~~a second latch circuit,~~

~~wherein a changing over means which can change over an electrical connection between~~
~~a signal line and the plurality of current sources is provided between the signal line and the~~
~~plurality of current sources, and~~

~~wherein the set signal is set according to the first shift register~~

first and second shift registers;

first and second latch circuits electrically connected to the first and second shift registers;

a plurality of current sources, each of which is configured to receive a set signal and to
control an output current value in response to the set signal;

a changing over means electrically connected to the plurality of current sources and a
plurality of signal lines,

wherein the changing over means selects one current source from the plurality of current
sources for electrically connecting to each of the plurality of signal lines, and

wherein the set signal is an output of the first shift register.

8. (Currently amended) A signal line driver circuit comprising: ~~characterized in by~~
~~comprising a plurality of current sources which can control an output current value by a set~~
~~signal inputted from outside, a first shift register, a second shift register, a first latch circuit, a~~
~~second latch circuit, and switches provided in the plurality of current sources,~~

~~wherein a changing over means which can change over an electrical connection between~~
~~a signal line and the plurality of current sources is provided between the signal line and~~
~~the plurality of current sources,~~

~~wherein the set signal is set according to the first shift register, and~~

~~wherein the switches are controlled by a signal from the second latch circuit.~~

first and second shift registers;

first and second latch circuits electrically connected to the first and second shift registers;
a plurality of current sources, each of which is configured to receive a set signal and to
control an output current value in response to the set signal;
a changing over means electrically connected to the plurality of current sources and a
plurality of signal lines; and
a switch provided between the changing over means and each of the plurality of current
sources,
wherein the changing over means selects one current source from the plurality of current
sources for electrically connecting to each of the plurality of signal lines,
wherein the set signal is an output of the first shift register, and
wherein the switch is controlled by a signal from the second latch circuit.

9. (Currently amended) A signal line driver circuit comprising: ~~characterized in by~~
~~comprising a plurality of first and second current sources which can control an output current~~
~~value by a set signal inputted from outside, a shift register, a first latch circuit comprising the~~
~~first current source, and a second latch circuit comprising the second current source,~~
~~wherein a changing over means which can change over an electrical connection between~~
~~a signal line and the plurality of first and second current sources is provided between the~~
~~signal line and the plurality of first and second current sources,~~
~~wherein a set signal inputted to the first current source is set according to from the shift~~
~~register, and~~
~~wherein a set signal inputted to the second current source is set according to a current~~
~~set in the first current source~~
a shift register;
a first latch circuit, electrically connected to the shift register, comprising a plurality of
first current sources, each of which is configured to receive a first set signal and to control an
output current value in response to the first set signal;

a second latch circuit comprising a plurality of second current sources, each of which is configured to receive a second set signal and to control an output current value in response to the second set signal; and

a changing over means electrically connected to the plurality of first current sources, the plurality of second current sources and a plurality of signal lines;

wherein the changing over means selects one first current source from the plurality of first current sources and one second current source from the plurality of second current sources for electrically connecting to each of the plurality of signal lines,

wherein the first set signal is an output of the first shift register, and

wherein the second set signal is a current outputted from the selected first current source.

10. (Currently amended) A signal line driver circuit comprising: ~~characterized in by comprising a plurality of first and second current sources which can control an output current value by a set signal inputted from outside, a shift register, a first latch circuit comprising the first current source, a second latch circuit comprising the second current source, a first switch provided between the first latch circuit, and the second latch circuit and a second switch provided between the second latch circuit and the changing over means,~~

~~wherein a changing over means which can change over an electrical connection between a signal line and the plurality of first and second current sources is provided between the signal line and the plurality of first and second current sources,~~

~~wherein a set signal inputted to the first current source is set according to from the shift register,~~

~~wherein a set signal inputted to the second current source is set according to a current set in the first current source, and~~

~~wherein the first and the second switches are controlled by a signal from the second latch circuit~~

a shift register;

a first latch circuit, electrically connected to the shift register, comprising a plurality of first current sources, each of which is configured to receive a first set signal and to control an output current value in response to the first set signal;

a second latch circuit comprising a plurality of second current sources, each of which is configured to receive a second set signal and to control an output current value in response to the second set signal;

a changing over means electrically connected to the plurality of second current sources and a plurality of signal lines;

a first switch provided between each of the plurality of first current sources and each of the plurality of second current sources; and

a second switch provided between the changing over means and each of the second plurality of current sources,

wherein the changing over means selects one first current source from the plurality of first current sources and one second current source from the plurality of second current sources for electrically connecting to each of the plurality of signal lines,

wherein the first set signal is an output of the first shift register,

wherein the second set signal is a current outputted from the selected first current source,

and

wherein the first and second switches are controlled by a signal from the second latch circuit.

11. (Currently amended) A signal line driver circuit comprising: ~~characterized in by comprising a plurality of pairs of current sources which can control an output current value by a set signal inputted from outside, a shift register, and a latch circuit comprising the pair of current sources;~~

~~wherein a changing over means which can change over an electrical connection between a signal line and the plurality of pairs of current sources is provided between the signal line and the plurality of pairs of current sources, and~~

~~wherein a set signal inputted to the pair of current sources is set according to from the shift register~~

a shift register;

a latch circuit, electrically connected to the shift register, comprising a plurality of pairs of current sources, each of which is configured to receive a set signal and to control an output current value in response to the set signal;

a changing over means electrically connected to the plurality of pairs of current sources and a plurality of signal lines;

wherein the changing over means selects one pair of current sources from the plurality of pairs of current sources for electrically connecting to each of the plurality of signal lines, and

wherein the set signal is an output of the shift register.

12. (Currently amended) A signal line driver circuit comprising: ~~characterized in by comprising a plurality of pairs of current sources which can control an output current value by a set signal inputted from outside, a shift register, a latch circuit comprising the pair of current sources and first and second switches,~~

~~wherein a changing over means which can change over an electrical connection between a signal line and the plurality of pairs of current sources is provided between the signal line and the plurality of pairs of current sources;~~

~~wherein a set signal inputted to the pair of current sources is set according to from the shift register, and~~

~~the first and second switches are controlled by a latch pulse~~

a shift register;

a latch circuit, electrically connected to the shift register, comprising:

a plurality of pairs of current sources, each of which is configured to receive a set signal and to control an output current value in response to the set signal;

a first switch provided between the shift register and each of the plurality of pairs of current sources; and

a second switch provided between each of the plurality of pairs of current sources and the changing over means,

a changing over means electrically connected to the plurality of pairs of current sources and a plurality of signal lines;

wherein the changing over means selects one pair of current sources from the plurality of pairs of current sources for electrically connecting to each of the plurality of signal lines,

wherein the set signal is an output of the shift register, and

wherein the first and second switches are controlled by a latch pulse.

13. (Previously Presented) The signal line driver circuit according to claim 3 characterized in that the changing over means comprises a plurality of analog switches, and that the current source is connected to the signal line through the analog switch.

14. (Previously Presented) The signal line driver circuit according to claim 3 characterized in that the changing over means comprises three analog switches for each of the signal line, and

that the each of the three analog switches is connected to the different current sources.

15. (Previously Presented) The signal line driver circuit according to claim 3 characterized in that the changing over means comprises a group of analog switches formed by a plurality of analog switches and a group of current source circuit formed by a plurality of the current sources.

16. (Previously Presented) A light emitting device characterized in by comprising the signal line driver circuit according to claim 3.

17. (Previously Presented) A light emitting device characterized in by comprising two of the signal line driver circuit according to claim 3 and a pixel portion,

wherein the two signal line driver circuits have a function to input a difference of currents supplied from current sources of each to the pixel portion.

18. (Previously Presented) A light emitting device characterized in by comprising the signal line driver circuit according to claim 3 and a pixel portion,

wherein in the pixel portion, the signal line and a plurality of scan lines are aligned in matrix,

wherein a light emitting element is disposed at an intersection of the signal line and the scan line, and

wherein a transistor for switching which controls a current from the signal line and a transistor for controlling current which controls a current to be supplied to the light emitting element are provided.

19. (Currently amended) A driving method of a signal line driver circuit comprising the steps of: ~~characterized in by comprising a current source circuit comprising a plurality of current sources, a means for setting a current of the plurality of current sources, a plurality of signal lines through which the set current flows, and a changing over means provided between the signal line and the current source,~~

~~wherein the changing over means changes over a connection of the signal line and the current source circuit in each certain period~~

inputting a set signal to a current source; and

changing an electrical connection between a first signal line and the current source to an electrical connection between a second signal line and the current source after a certain period.

20. (Original) The driving method of a signal line driver circuit according to Claim 19 characterized in that the certain period is provided in a unit of frame period corresponding to a synchronized timing of a video signal inputted to the signal line.

21. (Original) The driving method of a signal line driver circuit according to Claim 19 characterized in that a unit of frame period corresponding to a synchronized timing of a video signal inputted to the signal line comprises a write period, and
wherein the certain period is provided so as not to overlap the write period.

22. (Original) The driving method of a signal line driver circuit according to Claim 19 characterized in that a unit of frame period corresponding to a synchronized timing of a video signal inputted to the signal line comprises m (m is a natural number of 2 or more) subframe periods SF1, SF2, . . . , SF m , and that the certain period is provided in the subframe period.

23. (Original) The driving method of a signal line driver circuit according to claim 19 characterized in that a unit of frame period corresponding to a synchronized timing of a video signal inputted to the signal line comprises m (m is a natural number of 2 or more) subframe periods SF1, SF2, . . . , SF m , and the subframe periods SF1, SF2, . . . , SF m each comprises write periods Ta1, Ta2, . . . , Ta m and display periods Ts1, Ts2, . . . , Ts m , and that the certain period is provided in the subframe period.

24. (Original) The driving method of a signal line driver circuit according to claim 19 characterized in that a period for performing a set operation by a means for setting a current of the plurality of current sources is provided in the certain period.

25. (Original) The driving method of a signal line driver circuit according to claim 24 characterized in that an operation to change over an electrical connection between the signal line and the current source and the set operation are not overlapped with each other in the certain period.

26. (Original) The driving method of a signal line driver circuit according to claim 25 characterized in that the operation to change over an electrical connection between the signal line and the current source is provided after the set operation in the certain period.

27. (Previously Presented) The signal line driver circuit according to claim 4 characterized in that the changing over means comprises a plurality of analog switches, and that the current source is connected to the signal line through the analog switch.

28. (Previously Presented) The signal line driver circuit according to claim 5 characterized in that the changing over means comprises a plurality of analog switches, and that the current source is connected to the signal line through the analog switch.

29. (Previously Presented) The signal line driver circuit according to claim 6 characterized in that the changing over means comprises a plurality of analog switches, and that the current source is connected to the signal line through the analog switch.

30. (Previously Presented) The signal line driver circuit according to claim 7 characterized in that the changing over means comprises a plurality of analog switches, and that the current source is connected to the signal line through the analog switch.

31. (Previously Presented) The signal line driver circuit according to claim 8 characterized in that the changing over means comprises a plurality of analog switches, and that the current source is connected to the signal line through the analog switch.

32. (Previously Presented) The signal line driver circuit according to claim 9 characterized in that the changing over means comprises a plurality of analog switches, and that the current source is connected to the signal line through the analog switch.

33. (Previously Presented) The signal line driver circuit according to claim 10 characterized in that the changing over means comprises a plurality of analog switches, and that the current source is connected to the signal line through the analog switch.

34. (Previously Presented) The signal line driver circuit according to claim 11 characterized in that the changing over means comprises a plurality of analog switches, and that the current source is connected to the signal line through the analog switch.

35. (Previously Presented) The signal line driver circuit according to claim 12 characterized in that the changing over means comprises a plurality of analog switches, and that the current source is connected to the signal line through the analog switch.

36. (Previously Presented) The signal line driver circuit according to claim 4 characterized in that the changing over means comprises three analog switches for each of the signal line, and

that the each of the three analog switches is connected to the different current sources.

37. (Previously Presented) The signal line driver circuit according to claim 5 characterized in that the changing over means comprises three analog switches for each of the signal line, and

that the each of the three analog switches is connected to the different current sources.

38. (Previously Presented) The signal line driver circuit according to claim 6 characterized in that the changing over means comprises three analog switches for each of the signal line, and

that the each of the three analog switches is connected to the different current sources.

39. (Previously Presented) The signal line driver circuit according to claim 7 characterized in that the changing over means comprises three analog switches for each of the signal line, and

that the each of the three analog switches is connected to the different current sources.

40. (Previously Presented) The signal line driver circuit according to claim 8 characterized in that the changing over means comprises three analog switches for each of the signal line, and

that the each of the three analog switches is connected to the different current sources.

41. (Previously Presented) The signal line driver circuit according to claim 9 characterized in that the changing over means comprises three analog switches for each of the signal line, and

that the each of the three analog switches is connected to the different current sources.

42. (Previously Presented) The signal line driver circuit according to claim 10 characterized in that the changing over means comprises three analog switches for each of the signal line, and

that the each of the three analog switches is connected to the different current sources.

43. (Previously Presented) The signal line driver circuit according to claim 11 characterized in that the changing over means comprises three analog switches for each of the signal line, and

that the each of the three analog switches is connected to the different current sources.

44. (Previously Presented) The signal line driver circuit according to claim 12 characterized in that the changing over means comprises three analog switches for each of the signal line, and

that the each of the three analog switches is connected to the different current sources.

45. (Previously Presented) The signal line driver circuit according to claim 4 characterized in that the changing over means comprises a group of analog switches formed by a plurality of analog switches and a group of current source circuit formed by a plurality of the current sources.

46. (Previously Presented) The signal line driver circuit according to claim 5 characterized in that the changing over means comprises a group of analog switches formed by a plurality of analog switches and a group of current source circuit formed by a plurality of the current sources.

47. (Previously Presented) The signal line driver circuit according to claim 6 characterized in that the changing over means comprises a group of analog switches formed by a plurality of analog switches and a group of current source circuit formed by a plurality of the current sources.

48. (Previously Presented) The signal line driver circuit according to claim 7 characterized in that the changing over means comprises a group of analog switches formed by a plurality of analog switches and a group of current source circuit formed by a plurality of the current sources.

49. (Previously Presented) The signal line driver circuit according to claim 8 characterized in that the changing over means comprises a group of analog switches formed by a plurality of analog switches and a group of current source circuit formed by a plurality of the current sources.

50. (Previously Presented) The signal line driver circuit according to claim 9 characterized in that the changing over means comprises a group of analog switches formed by a plurality of analog switches and a group of current source circuit formed by a plurality of the current sources.

51. (Previously Presented) The signal line driver circuit according to claim 10 characterized in that the changing over means comprises a group of analog switches formed by a plurality of analog switches and a group of current source circuit formed by a plurality of the current sources.

52. (Previously Presented) The signal line driver circuit according to claim 11 characterized in that the changing over means comprises a group of analog switches formed by a plurality of analog switches and a group of current source circuit formed by a plurality of the current sources.

53. (Previously Presented) The signal line driver circuit according to claim 12 characterized in that the changing over means comprises a group of analog switches formed by a plurality of analog switches and a group of current source circuit formed by a plurality of the current sources.

54. (Previously Presented) A light emitting device characterized in by comprising the signal line driver circuit according to claim 4.

55. (Previously Presented) A light emitting device characterized in by comprising the signal line driver circuit according to claim 5.

56. (Previously Presented) A light emitting device characterized in by comprising the signal line driver circuit according to claim 6.

57. (Previously Presented) A light emitting device characterized in by comprising the signal line driver circuit according to claim 7.

58. (Previously Presented) A light emitting device characterized in by comprising the signal line driver circuit according to claim 8.

59. (Previously Presented) A light emitting device characterized in by comprising the signal line driver circuit according to claim 9.

60. (Previously Presented) A light emitting device characterized in by comprising the signal line driver circuit according to claim 10.

61. (Previously Presented) A light emitting device characterized in by comprising the signal line driver circuit according to claim 11.

62. (Previously Presented) A light emitting device characterized in by comprising the signal line driver circuit according to claim 12.

63. (Previously Presented) A light emitting device characterized in by comprising two of the signal line driver circuit according to claim 4 and a pixel portion,
wherein the two signal line driver circuits have a function to input a difference of currents supplied from current sources of each to the pixel portion.

64. (Previously Presented) A light emitting device characterized in by comprising two of the signal line driver circuit according to claim 5 and a pixel portion,
wherein the two signal line driver circuits have a function to input a difference of currents supplied from current sources of each to the pixel portion.

65. (Previously Presented) A light emitting device characterized in by comprising two of the signal line driver circuit according to claim 6 and a pixel portion,

wherein the two signal line driver circuits have a function to input a difference of currents supplied from current sources of each to the pixel portion.

66. (Previously Presented) A light emitting device characterized in by comprising two of the signal line driver circuit according to claim 7 and a pixel portion,

wherein the two signal line driver circuits have a function to input a difference of currents supplied from current sources of each to the pixel portion.

67. (Previously Presented) A light emitting device characterized in by comprising two of the signal line driver circuit according to claim 8 and a pixel portion,

wherein the two signal line driver circuits have a function to input a difference of currents supplied from current sources of each to the pixel portion.

68. (Previously Presented) A light emitting device characterized in by comprising two of the signal line driver circuit according to claim 9 and a pixel portion,

wherein the two signal line driver circuits have a function to input a difference of currents supplied from current sources of each to the pixel portion.

69. (Previously Presented) A light emitting device characterized in by comprising two of the signal line driver circuit according to claim 10 and a pixel portion,

wherein the two signal line driver circuits have a function to input a difference of currents supplied from current sources of each to the pixel portion.

70. (Previously Presented) A light emitting device characterized in by comprising two of the signal line driver circuit according to claim 11 and a pixel portion,

wherein the two signal line driver circuits have a function to input a difference of currents supplied from current sources of each to the pixel portion.

71. (Previously Presented) A light emitting device characterized in by comprising two of the signal line driver circuit according to claim 12 and a pixel portion,

wherein the two signal line driver circuits have a function to input a difference of currents supplied from current sources of each to the pixel portion.

72. (Previously Presented) A light emitting device characterized in by comprising the signal line driver circuit according to claim 4 and a pixel portion,

wherein in the pixel portion, the signal line and a plurality of scan lines are aligned in matrix,

wherein a light emitting element is disposed at an intersection of the signal line and the scan line, and

wherein a transistor for switching which controls a current from the signal line and a transistor for controlling current which controls a current to be supplied to the light emitting element are provided.

73. (Previously Presented) A light emitting device characterized in by comprising the signal line driver circuit according to claim 5 and a pixel portion,

wherein in the pixel portion, the signal line and a plurality of scan lines are aligned in matrix,

wherein a light emitting element is disposed at an intersection of the signal line and the scan line, and

wherein a transistor for switching which controls a current from the signal line and a transistor for controlling current which controls a current to be supplied to the light emitting element are provided.

74. (Previously Presented) A light emitting device characterized in by comprising the signal line driver circuit according to claim 6 and a pixel portion,

wherein in the pixel portion, the signal line and a plurality of scan lines are aligned in matrix,

wherein a light emitting element is disposed at an intersection of the signal line and the scan line, and

wherein a transistor for switching which controls a current from the signal line and a transistor for controlling current which controls a current to be supplied to the light emitting element are provided.

75. (Previously Presented) A light emitting device characterized in by comprising the signal line driver circuit according to claim 7 and a pixel portion,

wherein in the pixel portion, the signal line and a plurality of scan lines are aligned in matrix,

wherein a light emitting element is disposed at an intersection of the signal line and the scan line, and

wherein a transistor for switching which controls a current from the signal line and a transistor for controlling current which controls a current to be supplied to the light emitting element are provided.

76. (Previously Presented) A light emitting device characterized in by comprising the signal line driver circuit according to claim 9 and a pixel portion,

wherein in the pixel portion, the signal line and a plurality of scan lines are aligned in matrix,

wherein a light emitting element is disposed at an intersection of the signal line and the scan line, and

wherein a transistor for switching which controls a current from the signal line and a transistor for controlling current which controls a current to be supplied to the light emitting element are provided.

77. (Previously Presented) A light emitting device characterized in by comprising the signal line driver circuit according to claim 10 and a pixel portion,

wherein in the pixel portion, the signal line and a plurality of scan lines are aligned in matrix,

wherein a light emitting element is disposed at an intersection of the signal line and the scan line, and

wherein a transistor for switching which controls a current from the signal line and a transistor for controlling current which controls a current to be supplied to the light emitting element are provided.

78. (Previously Presented) A light emitting device characterized in by comprising the signal line driver circuit according to claim 11 and a pixel portion,

wherein in the pixel portion, the signal line and a plurality of scan lines are aligned in matrix,

wherein a light emitting element is disposed at an intersection of the signal line and the scan line, and

wherein a transistor for switching which controls a current from the signal line and a transistor for controlling current which controls a current to be supplied to the light emitting element are provided.

79. (Canceled) A light emitting device characterized in by comprising the signal line driver circuit according to claim 11 and a pixel portion,

wherein in the pixel portion, the signal line and a plurality of scan lines are aligned in matrix,

wherein a light emitting element is disposed at an intersection of the signal line and the scan line, and

wherein a transistor for switching which controls a current from the signal line and a transistor for controlling current which controls a current to be supplied to the light emitting element are provided.

80. (Previously Presented) A light emitting device characterized in by comprising the signal line driver circuit according to claim 12 and a pixel portion,

wherein in the pixel portion, the signal line and a plurality of scan lines are aligned in matrix,

wherein a light emitting element is disposed at an intersection of the signal line and the scan line, and

wherein a transistor for switching which controls a current from the signal line and a transistor for controlling current which controls a current to be supplied to the light emitting element are provided.